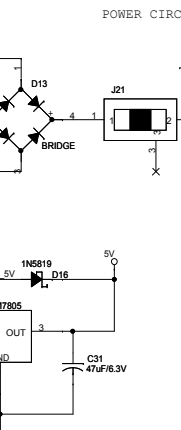
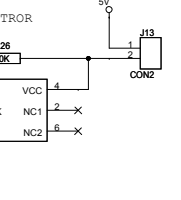
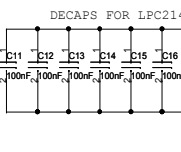


PROCESSOR

CONNECT	P0_A31	17	P0_31/UP_LED/CONNECT
ADC	P0_A30	16	P0_30/ADD_3/EINT3/CAP0.0
MMC_WP	P0_A29	15	P0_29/ADD_2/CAP0.3/MAT0.3
MMC_CP	P0_A28	14	P0_28/ADD_1/CAP0.2/MAT0.2
AOUT	P0_A25	9	P0_25/ADD_4/AOUT
USB_5_1	P0_A23	58	P0_23/VBUS
VGA_VSYNC	P0_A22	2	P0_22/CAPO_0/MAT0.0
VGA_HSYNC	P0_A21	1	P0_21/PWM5/CAP1.3
SSEL1	P0_A20	12	P0_20/MAT1_3/SSEL1/EINT3
MISO1	P0_A19	54	P0_19/MAT1_2/MISO1/CAP1.2
MOSI1	P0_A18	53	P0_18/CAP1_3/MISO1/MAT1.3
SOCK1	P0_A17	42	P0_17/CAP1_2/SCK1/MAT1.2
PS2_CLK	P0_A16	46	P0_16/EINT0/MAT0.2/CAP0.2
EINT1	P0_A15	45	P0_15/EINT2
BSL	P0_A14	41	P0_14/EINT1/SDA1
D7_LCD	P0_A13	38	P0_13/MAT1.1
DS_LCD	P0_A12	38	P0_12/MAT1.1
DS_LCD	P0_A11	37	P0_11/CAP1_1/SCL1
D4_LCD	P0_A10	36	P0_10/CAP1.0
RXD1	P0_A9	34	P0_9/RXD1/PWM5/EINT3
TXD1	P0_A8	33	P0_8/TXD1/PWM4
SSEL0	P0_A7	31	P0_7/SSEL0/PWM2/EINT2
MOSI0	P0_A6	30	P0_6/MISO0/CAP0.2
MISO0	P0_A5	29	P0_5/MISO0/MAT0.1/AD0.7
SCK0	P0_A4	27	P0_4/SCK0/CAP0.1/AD0.6
SDA0	P0_A3	26	P0_3/SDA0/MAT0.0/EINT1
SCL0	P0_A2	22	P0_2/SCL0/CAP0.0
RXD0	P0_A1	21	P0_1/RXD0/PWM3/EINT0
TXD0	P0_A0	18	P0_0/TXD0/PWM1



P1_16	TRACEPKT0	16	P1_A16	PS2_DATA
P1_17	TRACEPKT1	17	P1_A17	VGA_B
P1_18	TRACEPKT2	18	P1_A18	VGA_G
P1_19	TRACEPKT3	19	P1_A19	VGA_R
P1_20	TRACESYNC	20	P1_A20	TMS
P1_21	PIPESTAT0	21	P1_A21	LCD_BL
P1_22	PIPESTAT1	22	P1_A22	EN_LCD
P1_23	PIPESTAT2	23	P1_A23	RW_LCD
P1_24	TRACECLK	24	P1_A24	RS_LCD
P1_25	EXTN0	25	P1_A25	BUZZER
P1_26	RTCK	26	P1_A26	RTCK
P1_27	TDO	27	P1_A27	TDO
P1_28	TDI	28	P1_A28	TDI
P1_29	TCK	29	P1_A29	TCK
P1_30	TMS	30	P1_A30	TMS
P1_31	TRST	31	P1_A31	TRST

